Veer Narmad South Gujarat University, Surat

Proposed Syllabus

For

B. Sc. Semester IV

Physics (SEC) (NEP-2020)

Effective from the Academic Year 2024-25

Paper: PH -SEC-1 (Practical Only) (2 Credits)

SEC- Basic Electronics Circuit & Designing -II_Sem-4

1. Design and test AND Gate Using Diode.
2. Design and test OR Gate Using Diode.
3. Design and test NOT Gate Using Transistor.
4. Design and test AND Gate Using Transistor.
5. Design and test OR Gate Using Transistor.
6. Design and test Temperature to frequency Convertor Using IC-555 and Thermistor.
7. Design and test X-OR gate using NAND gate.
8. Design and test Ex-or gate using Basic logic gates.
9. Design and test Seven segment using LED.
10. To Determine the Plank's constant using LED.
11. Project work (Equivalent to 5 Experiment)

Reference:

Text book electrical Technology by B L Theraja, S Chand Co.

A text book of Electronics By Santanu Chattopadhyay

Asfale
24-10-24

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